

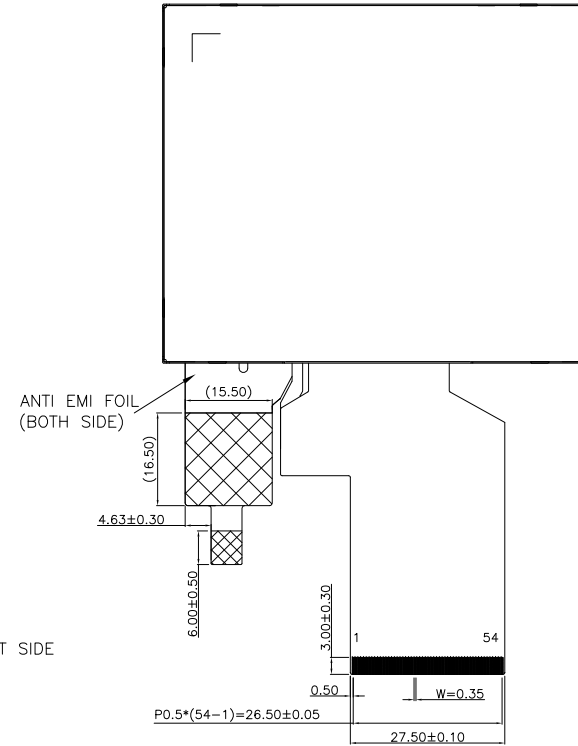
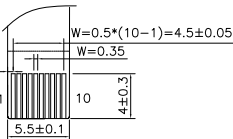
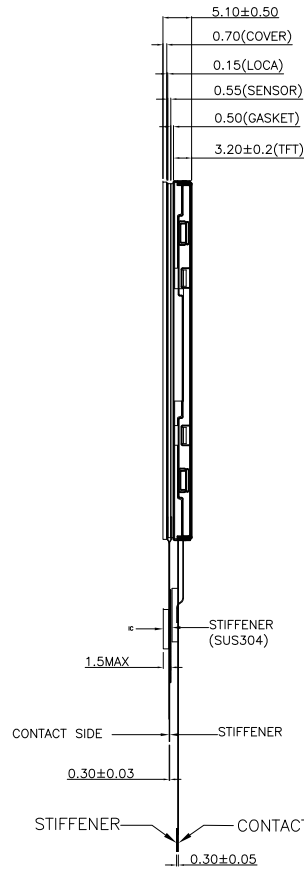
NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE.
2. OPERATING VOLTAGE: VDD=3.3V.
3. VIEWING DIRECTION: 12 O'CLOCK.
4. IC DRIVER: HX8238D.
5. OPERATING TEMP: -20°C ~ 70°C.
6. STORAGE TEMP: -30°C ~ 80°C.
7. LED BACKLIGHT: 6-CHIP WHITE LED, If=20mA, Vf=19.2V.
8. SURFACE LUMINANCE: 255cd/m²(TYP)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE.
10. GENERAL TOLERANCE: ±0.2.
11. RoHS COMPLIANT.

NOTES FOR CTP:

1. 3.5 INCH PROJECTIVE CAPACITIVE TOUCH PANEL.
2. OPERATING VOLTAGE: VCC=3.3V.
3. COVER GLASS + SENSOR GLASS + FPCA.
4. RESOLUTION: 320 X 240 DOTS.
5. IC DRIVER: FT5446-P03.
6. MULTI FINGER: UP TO 5.
7. HARDNESS OF COVER SURFACE: 7H.

DETAIL A: 30:1



LEDA → → → → → LEDK

CIRCUIT DIAGRAM

PIN ASSIGNMENT

1	LEDK	29	R1
2	LEDK	30	R2
3	LEDA	31	R3
4	LEDA	32	R4
5	NC	33	R5
6	NC	34	R6
7	NC	35	R7
8	RESET	36	HSYNC
9	SPENA	37	VSYNC
10	SPCLK	38	DCLK
11	SPDAT	39	NC
12	B0	40	NC
13	B1	41	VDD
14	B2	42	VDD
15	B3	43	NC
16	B4	44	NC
17	B5	45	NC
18	B6	46	NC
19	B7	47	NC
20	G0	48	SEL2
21	G1	49	SEL1
22	G2	50	SELO
23	G3	51	NC
24	G4	52	DEN
25	G5	53	GND
26	G6	54	GND
27	G7		
28	RO		

PIN ASSIGNMENT FOR CTP

1	VSS
2	VCC
3	SCL(SSEL)
4	NC(SCK)
5	SDA(MOSI)
6	NC(MISO)
7	RST
8	NC
9	INT
10	VSS

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	RITA	2016.02.23	SCALE : 1/1
CHECKED BY:			UNIT: mm
APPROVED BY:			DESCRIPTION: TFT & CTP MODULE
VERSION NO:	03		
DRAWN BY:		MODULE P/N:	
CHECKED BY:		MI0350AGT-1CP2	

03	UPDATE CTP IC DRIVER FROM FT5446D0S TO FT5446-P03.	CAROL	2020.06.28
02	OPTIMIZE SENSOR STRUCTURE DESIGN	RITA	2017.09.01
01	FIRST ISSUE	RITA	2017.02.23
VER.	REVISED DESCRIPTION	REVISER	DATE